

## PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE  $^{5}$ 

In re the Application of:

HIRANO et al.

Art Unit: 2811

Application No.: 09/813,866

Examiner: Samuel A. Gebremakia

Filed: March 22, 2001

Atty. Dkt. No.: 107318-00000

For: SEMICONDUCTOR DEVICE DISPLAY DEVICE AND METHOD OF

FABRICATING THE SAME

## RESPONSE UNDER 37 C.F.R. § 1.111

Commissioner for Patents Washington, D.C. 20231

Date: August 1, 2002

Sir:

This paper is filed in response to the Office Action dated April 1, 2002, the time period for response having been extended until August 1, 2002, by the attached Petition for Extension of Time. In the prosecution of the application identified above, please consider the following amendment.

**IN THE CLAIMS**:

Please cancel claims 44-59.

Please add new claims 60-69 as follows:

60. (New) A method of fabricating a semiconductor device, comprising the

steps of:

forming an amorphous silicon film on a substrate;

TECH/128600.1